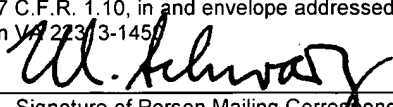


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors:	Vermeire, F.G.C.)	
	Skrzeszewski, T.K.)	Group Art Unit: not assigned yet
)	
Title:	Pipelined Processor and)	Examiner: not assigned yet
	Instruction Loop Method)	
)	
Filed:	February 20, 2004 (con of)	
	PCT/NL02/0556, filed 08/22/2002)	

CERTIFICATE OF MAILING	
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PRELIMINARY AMENDMENT

Please amend the above application to add the following:

-- CROSS REFERENCE TO RELATED APPLICATION

This application is a continuation of PCT/NL02/0556, filed on August 22, 2002 and published under Article 21(2) in English. --

Respectfully submitted



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